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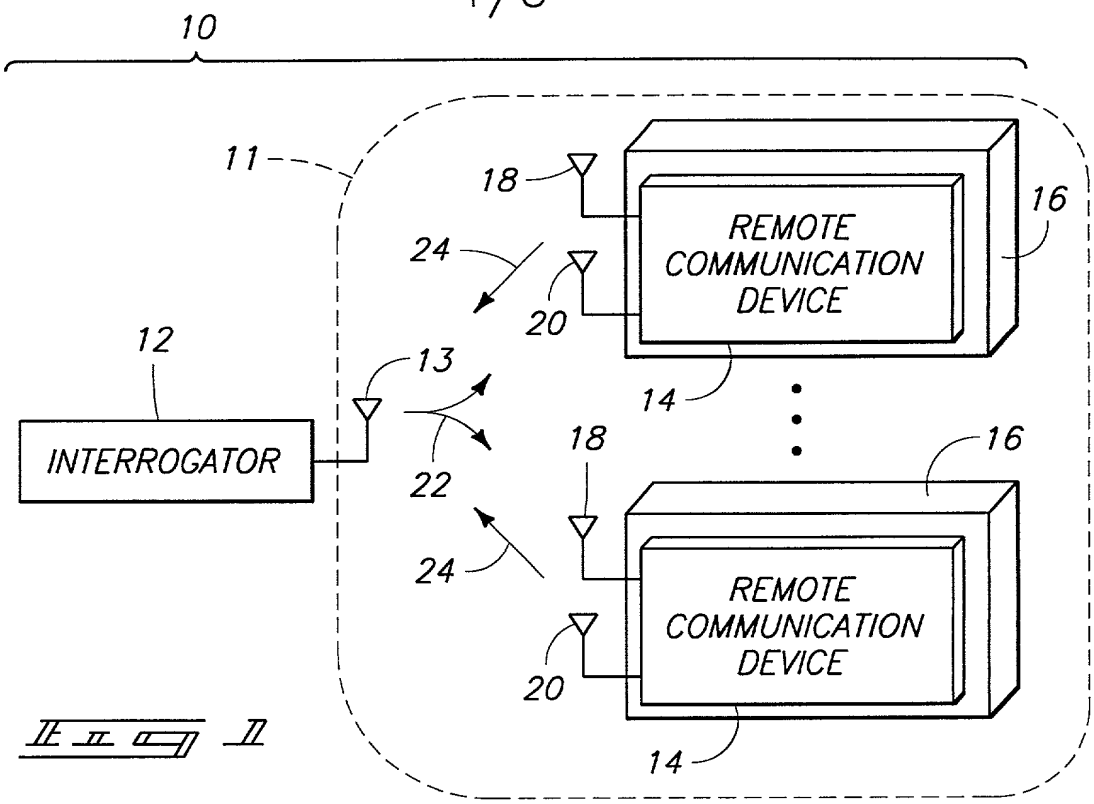

Fig 1

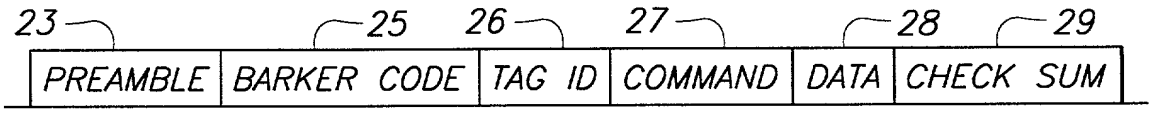

Fig 2

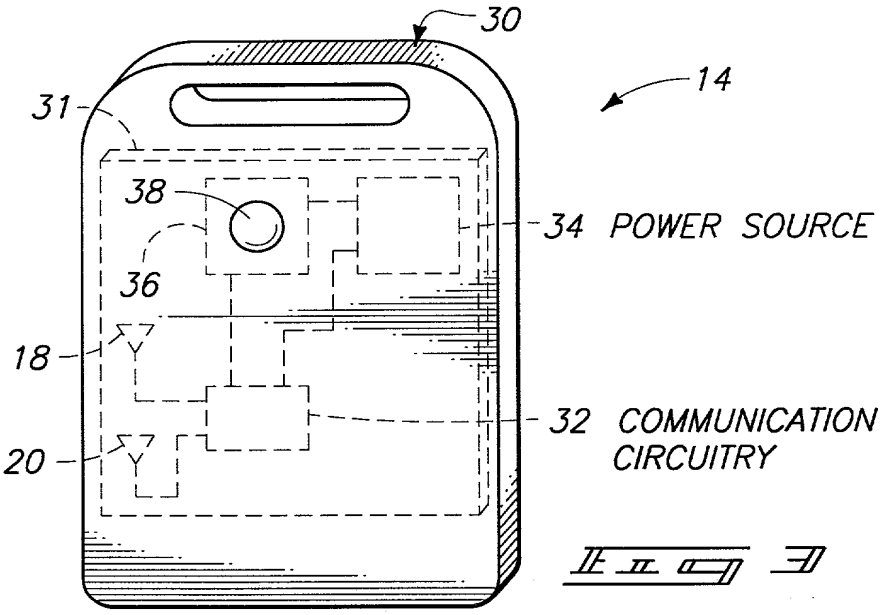

Fig 3

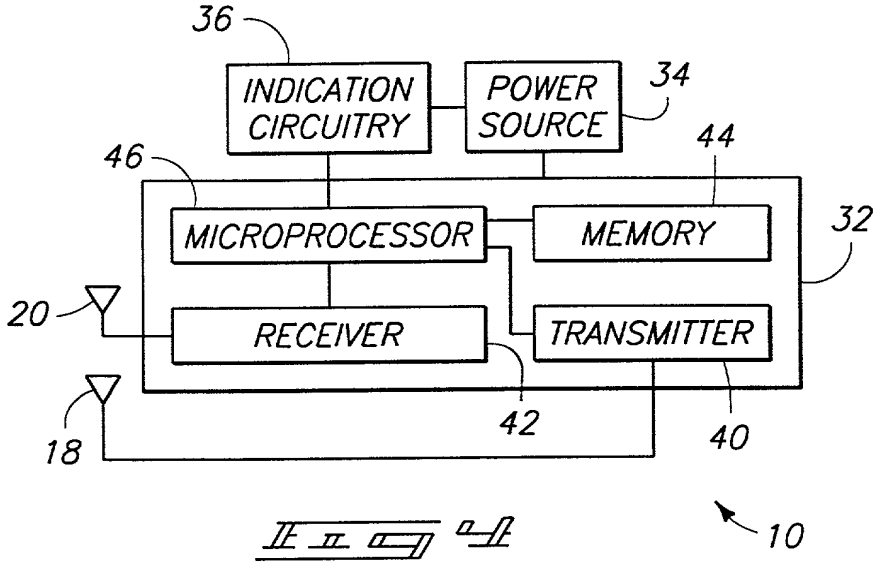
36
INDICATION CIRCUITRY
POWER SOURCE
34
44
46
MICROPROCESSOR
MEMORY
32
20
RECEIVER
TRANSMITTER
42
40
18
10

Fig 4

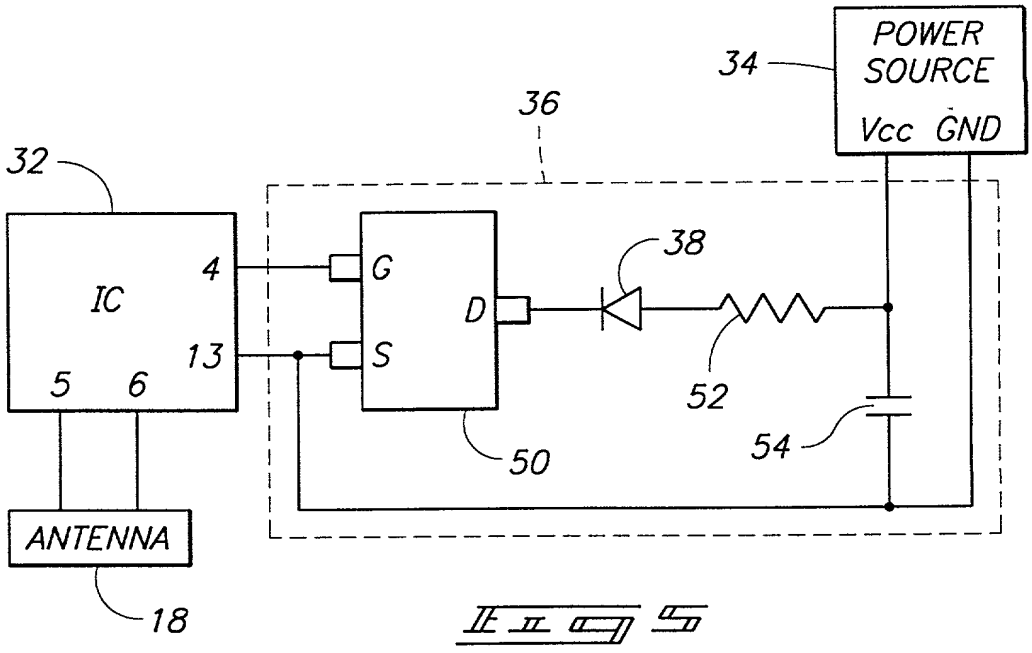
34
POWER SOURCE
Vcc GND
36
32
IC
4
13
5
6
G
S
D
38
52
54
50
ANTENNA
18

Fig 5

AS FILED

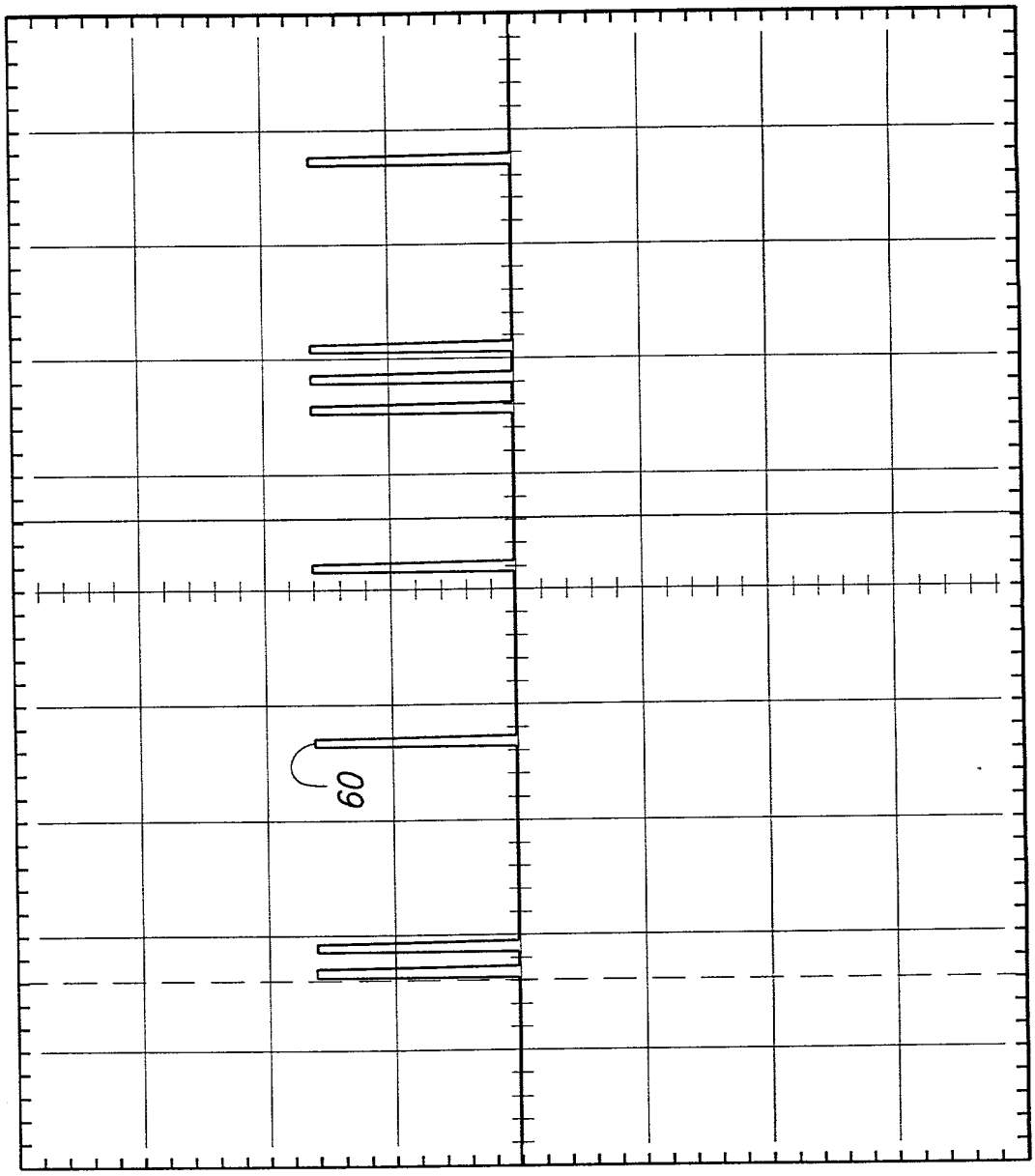

FIG 5

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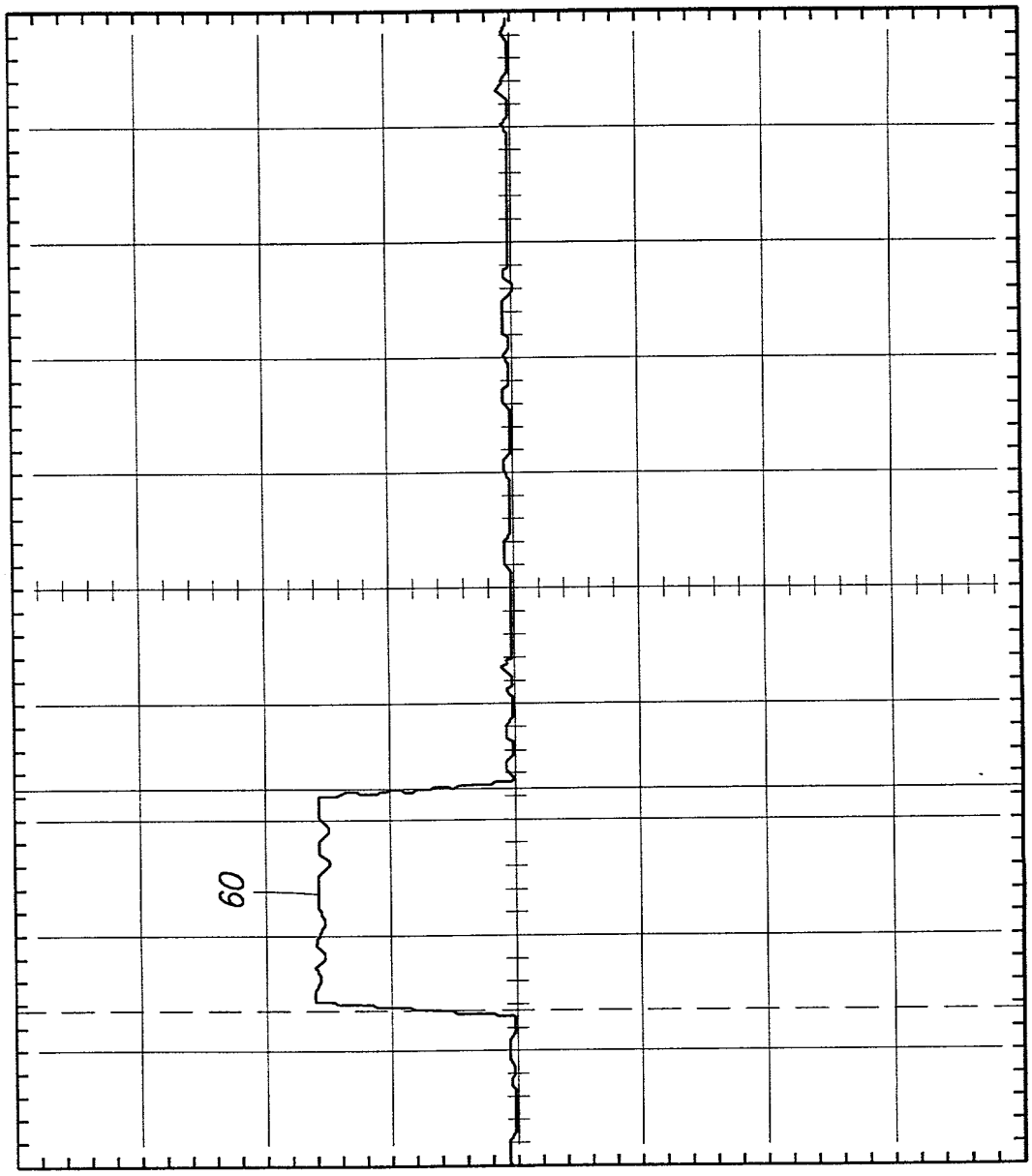
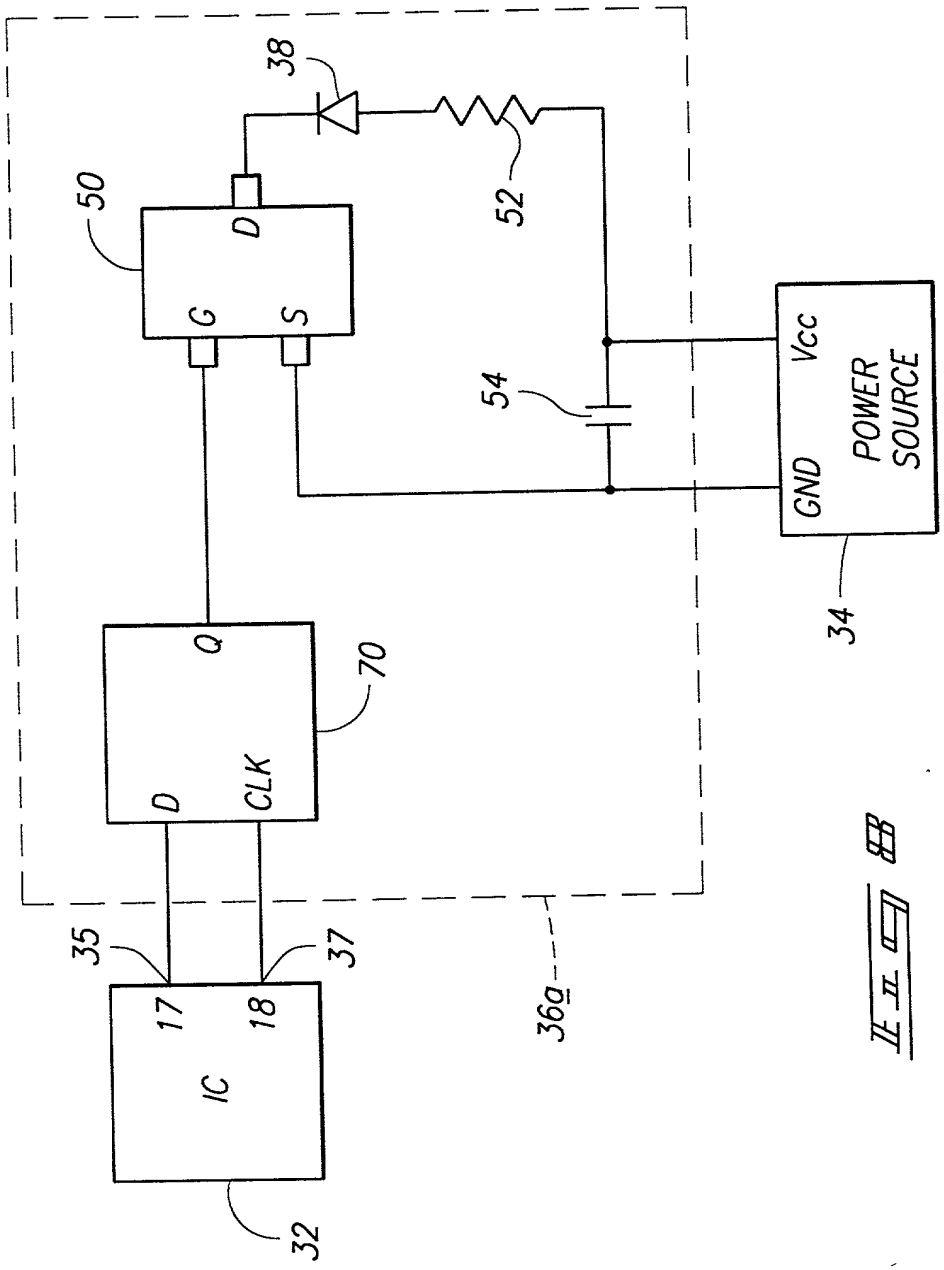


FIG 5

PROVIDED SUBJECT TO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S
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Vcc
POWER SOURCE
GND
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Q
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D
CLK
35
37
17
18
IC
32
36a

FIG 8